Appl. No. 09/982,459
Amdt. dated: May 13, 2004
Reply to Final Office Action of January 13, 2004

Docket No. SUN-P5405
(811173-000073)

AMENDMENTS TO THE SPECIFICATION

Please insert the following section after the title of the invention on page 2 of the present specification:

CROSS-REFERENCE TO RELATED APPLICATIONS

This application is related to U.S. Patent Application Serial No. 09/982,452, entitled "Clock Skew Verification Methodology for Grid-Based Design," in the name of Manjunath D. Haritsa, Manishkumar B. Ankola, Ralf M. Schmitt, Anup Sharma, Stephen Hoerold, and David M. Murata, now published as 2003/0074642A1, and U.S. Patent Application Serial No. 09/982,458, entitled "Simulation by Parts method for Grid-Based Clock Distribution Design," in the name of Manjunath D. Haritsa and Ralf M. Schmitt, now published as 2003/0074175A1, both filed on an even date herewith and commonly assigned wherewith.